5E3251

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B.Tech. V SEM (Main/Back) Examination Dec.2012
Computer Science
5CS1-Computer Architecture
Common Computer & IT

Time : 3 Hours

Maximum Marks : 80
Min. Passing Marks : 32

Instructions to Candidates:

*Attempt any **five** questions. Selecting **one** Question from **each unit**.*
*All questions carries **equal** marks. Schematic diagram must be*
shown wherever necessary. Any feel suitable be assumed and stated
clearly. Units of Quantities used/calculated must be stated clearly.

Use of following supporting material is permitted during examination.
Mentioned in form No. 205)

1.	Nil	2.	Nil

UNIT-I

1. (a) List at least five essential functional blocks that any computer should possess. Describe briefly the role of each block.   (8)

   (b) Define three state bus buffers. What do you understand by High Impedance state in three state buffers?   (8)

OR

(a) Describe the Von-Newman Model & functioning.   (8)

   1. Memory address Register.

   2. Instruction Register.

   3. Program Counter.